

ABSTRACT

Provided is a data input/output buffer and a semiconductor memory device using the same. A transistor of a switching means or a logical element out of devices constituting a data input/output buffer, to which a data signal is
5 inputted most rapidly, is implemented using a low voltage-driven device whose threshold voltage is low. It is therefore possible to improve reliability of a circuit by exactly determining the level of a data signal even if the data signal is inputted as a level that is lowered as much as a threshold voltage.